



FORM HDP-1449 (Based on Form PTO-1449)

**PATENT AND TRADEMARK OFFICE
INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

Sheet 1 of 5

ATTORNEY DOCKET NO.

MP0400

SERIAL NO.

10/693,787

APPLICANT

Sehat Sutardja

FILING DATE

10/24/2003

GROUP

2838

U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.		2001/0052837	12/2001	Walsh		
2.		2002/0039061	4/2002	Timashov		
3.		2003/0227366 A1	12/2003	Lin		
4.		2006/0116623	6/2006	Han et al.		
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10.		4,214,198 A	07/1980	Schneider		
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18.		4,801,912	1/1989	McElheny et al.		
19.		4,803,609	2/1989	Gillett et al.		
20.		5,186,647	2/1993	Denkmann et al.		

Examiner: /Rajnikant Patel/

Date Considered: 5/30/08

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2.		EP0895257 A	2/3/1999	Europe		X	
3.		DE3622190 A	1/7/1988	German		Abstract	
4.		GB2318691 A	4/29/1998	Great Britain		X	
5.		JP20020570039 A	2/2002	Japan		Abstract	

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3.		European Search Report for Application No. 04020571.8, 3 pages
4.		European Search Report for Application No. 04020568.4, 3 pages

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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)		
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5.		European Search Report for Application No. 04010841; 2 pages
6.		European Search Report for Application No. 04011558.6, 2 pages
7.		Power Converter Topologies, How to Choose the Right One for Your Design; Dennis L. Feucht; Innovatia Laboratories; 2002; 4 Pages.
8.		U.S. Patent Application Serial No. 10/621,128, filed July 16, 2003, entitled, "Power Inductor With Reduced DC Current Saturation", pp. 1-33, and 6 sheets of drawings.
9.		U.S. Patent Application Serial No. 10/693,787, filed October 24, 2003, entitled, "Voltage Regulator", pp. 1-104, and 11 sheets of drawings.
10.		U.S. Patent Application Serial No. 10/744,416, filed December 22, 2003, entitled, "Power Inductor With Reduced DC Current Saturation", pp. 1-39, and 9 sheets of drawings.
11.		U.S. Patent Application Serial No. 10/754,187, filed January 8, 2004, entitled, "Digital Low Dropout Regulator", pp. 1-85, and 19 sheets of drawings.
12.		Hayt and Kemmerly, "Engineering Circuit Analysis", 1986, Mcgraw-Hill Book Company, Fourth Edition, pages 442-443.

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